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| DONGWEI CHEN | | |
| Room 623, Building 29, 5 Yiheyuan Road, Haidian District, Beijing, China  (+86)15101650288 | cdwdhr@pku.edu.cn | | |
| **EDUCATION** | | |
| **PhD. of Science of Computer Science, Peking University** | | 2015 – Present |
| * Working on PhD degree in Microprocessor Research and Development Center of Peking University . **GPA: 3.54** * Now focusing on the non-blocking cache and memory system. | | |
| |  |  | | --- | --- | | **Bachelor of Science of Computer Science, Peking University** | 2011 – 2015 | | * School of Electronics Engineering and Computer Science. **GPA: 3.43** * **Relevant Coursework:** Data Structure and Algorithm, Algorithm Design and Analysis, Operating System, Compiler Design, Computer Architectures, Advanced Mathematics, Advanced Algebra, Probability Theory and Statistics, Set Theory and Graph Theory | | | | |
| |  |  | | --- | --- | | **Bachelor of Psychology as Double Major, Peking University** | 2012 – 2015 | | * **GPA: 3.02** * **Relevant Coursework:** Social Psychology, General Psychology, Cognitive Psychology, Development Psychology, Abnormal Psychology, Functional Anatomy of Central Nervous System | | | | |
| **EXPERIENCE** | | |
| **Research on the Hierarchical Memory System in RISCV Rocket-Chip** | | 2016-Present |
| * Built a test system for comparing modules with reference modules. * Now focusing on the cache coherence framework, TileLink. | | |
| **A 8-bit Processor Designed for Carbon Nanotube as Graduation Project** | | 2015 |
| * Designed a 8-bit processor with Intel 8008 as reference * The processor was implemented with Verilog, simulated with Modelsim and can run simple programs. | | |
| **A Simulator of MIPS Architecture** | | 2013 |
| * Implemented a simulator of CPU using a subset of MIPS architecture with C++. * The simulator can simulate the working process of a pipelined CPU with a cache. | | |
| **AWARDS** | | |
| WA Scholarship  3rd Award in *ACM programming competition of Peking University* | 2016  2012 | |
| **SKILLS** | | |
| **Technical Skills:** C, C++, Python, Verilog  **English:** Passed CET4(633) and CET6(607).  **Other Languages:** Chinese(Native speaker) | | |
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